

CMPE212 Lab9- Implementation of MUX

Objective

Implementation of Multiplexer in both Verilog and circuit.

Instructions-

1. Create the module file of a 8x1 MUX in Verilog (no need to write testbench file and simulate)
2. Draw the schematic of a 16x1 MUX.
3. Implement a 4 x 1 MUX in your breadboard.
4. Show the results to the TAs.

Remarks

- Connect the DIP switch properly.
- Connect resistors in series to your output LED.
- Don't forget to return the breadboards and multimeters with cables.