CMPE212 Lab1 Verilog Setup and Hardware Familiarity

Objective

To meet our lab partners. To setup Cadence so we can write Verilog code for the rest of the semester. Look at the pretty hardware that we'll be playing with this semester.

Icebreaker

Say 'hi' to your lab partner, exchange contact information. Maybe chat a bit and get to know one another.

Verilog

1. Connect to the Linux GL server using whichever terminal program you prefer, i.e. putty.exe

2. Use your favorite editor to open the **.cshrc** file, i.e. **nano .cshrc**

3. Add the following line TO THE END of your **.cshrc** file, then save and exit:
   source /afs/umbc.edu/software/cadence/etc/setup_2008/cshrc.cadence

4. Create a **cadence** directory, i.e. **mkdir cadence**

5. Go into the **cadence** directory, i.e. **cd cadence**

6. Perform the following commands:
   cp /afs/umbc.edu/software/cadence/etc/setup_2008/cds.lib .
   cp /afs/umbc.edu/software/cadence/etc/setup_2008/hdl.var .
   cp /afs/umbc.edu/software/cadence/etc/setup_2008/.cdsinit .
   cp /afs/umbc.edu/software/cadence/etc/setup_2008/.simrc .

7. Perform the following command:
   source ~/.cshrc

8. Verify that Verilog is installed by performing the following command:
   which verilog

Hardware

For this portion of the lab, gawk in awe at the power supply, function generator, oscilloscope, and cables. For those with courage, turn them on and play around with them. Try connecting the function generator to the oscilloscope and see all the pretty waveforms.

Conclusion

Have fun and ask lots of questions.